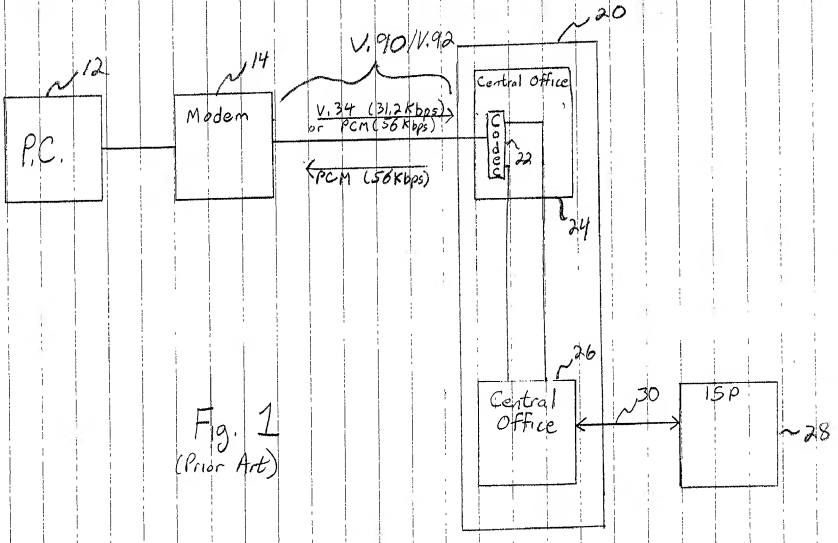

Fig. 1
(Prior Art)

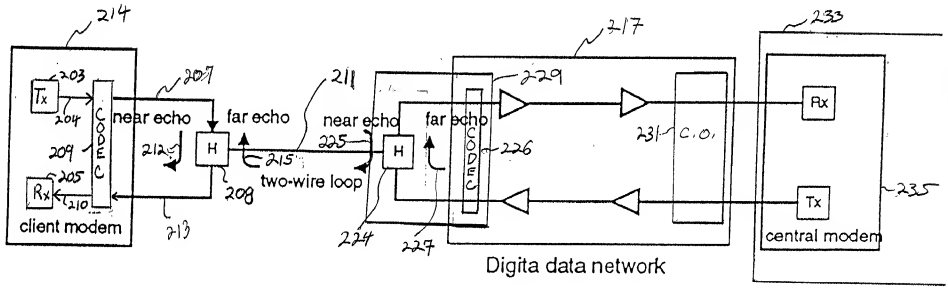

Figure 2. (PRIOR ART)

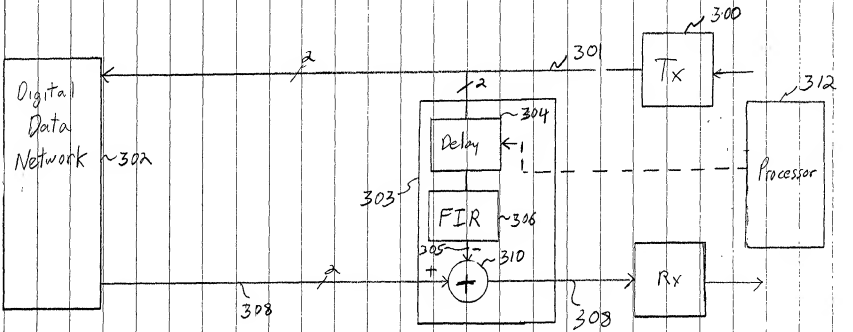

Figure 3 (Prior Art)

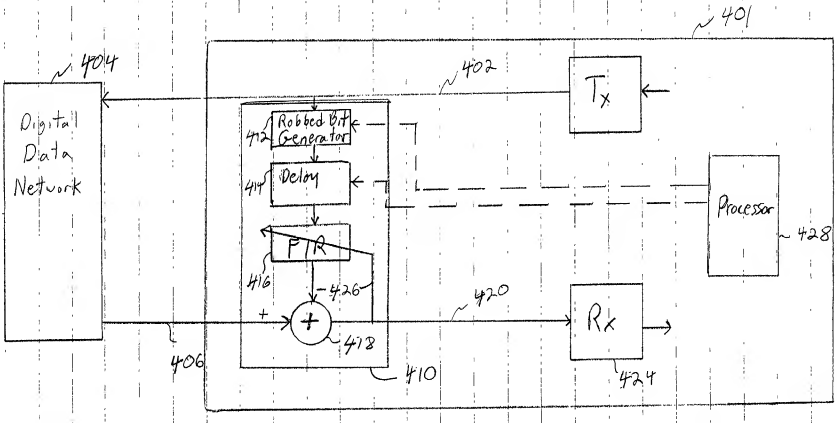
401
404
Digital Data Network
402
Tx
Robbed Bit Generator
412
Delay
414
Processor
428
FIR
416
426
420
Rx
+
418
406
410
424

Figure 4

TD90H0: 2E98E860

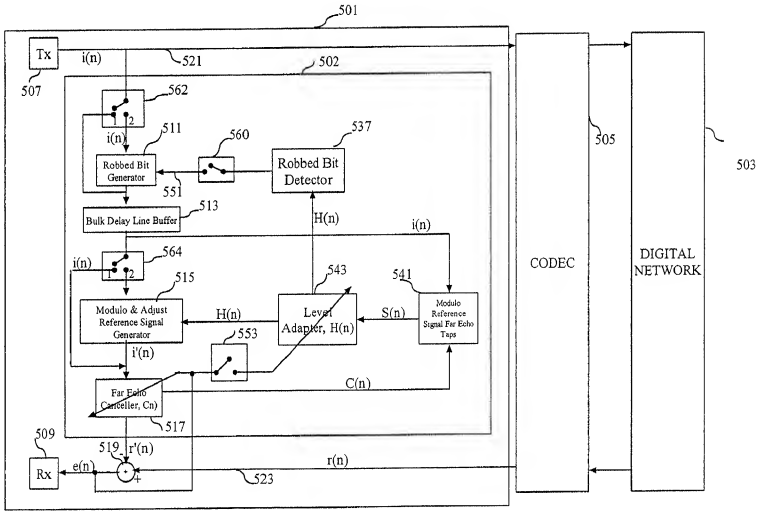

FIGURE 5